

**Broadband PIN Diode Attenuator Bias Network**

**ABSTRACT OF THE DISCLOSURE**

Diode network configurations are disclosed in which cathode bias voltage is held substantially constant to provide an attenuator circuit in which return loss is optimized throughout a broad dynamic attenuation range. Preferred embodiments include PIN diodes arranged in a  $\pi$  network having two attenuation control signals provided thereto. Alternative  
5 embodiments include PIN diodes arranged in a T network having two attenuation control signals provided thereto.